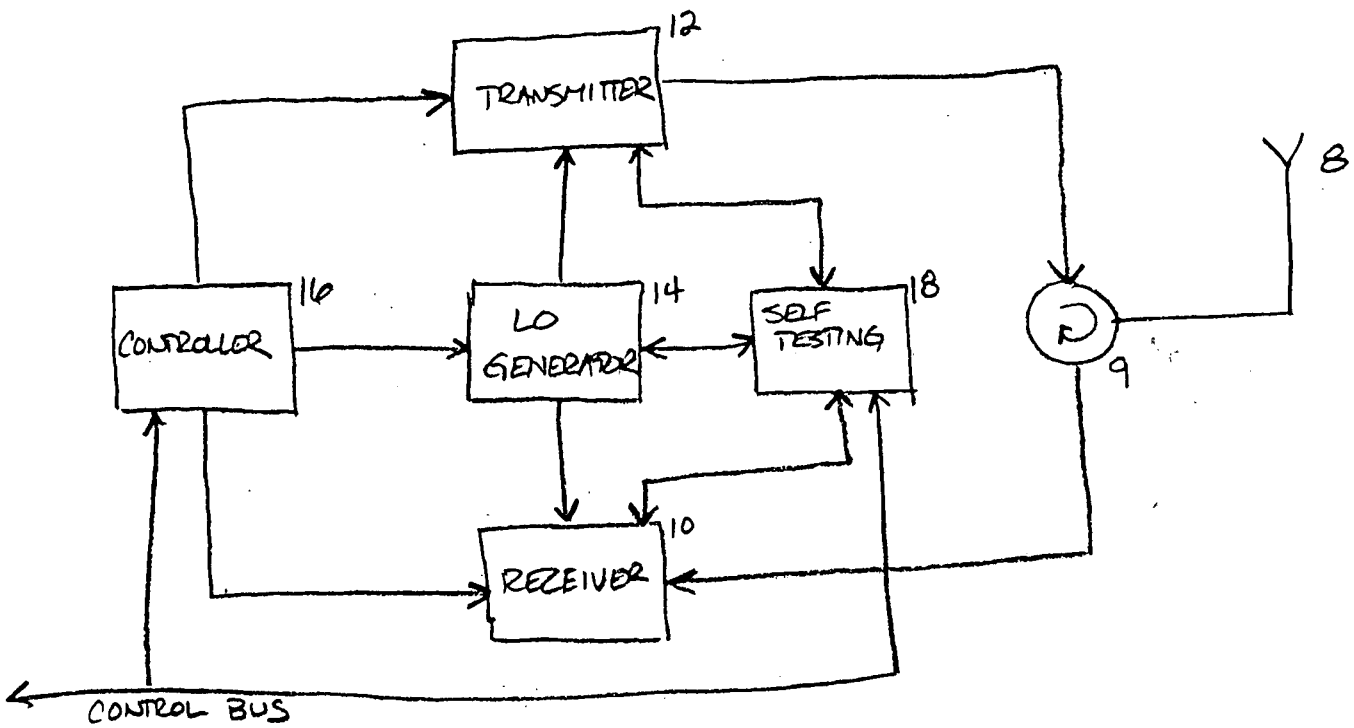


FIG. 1

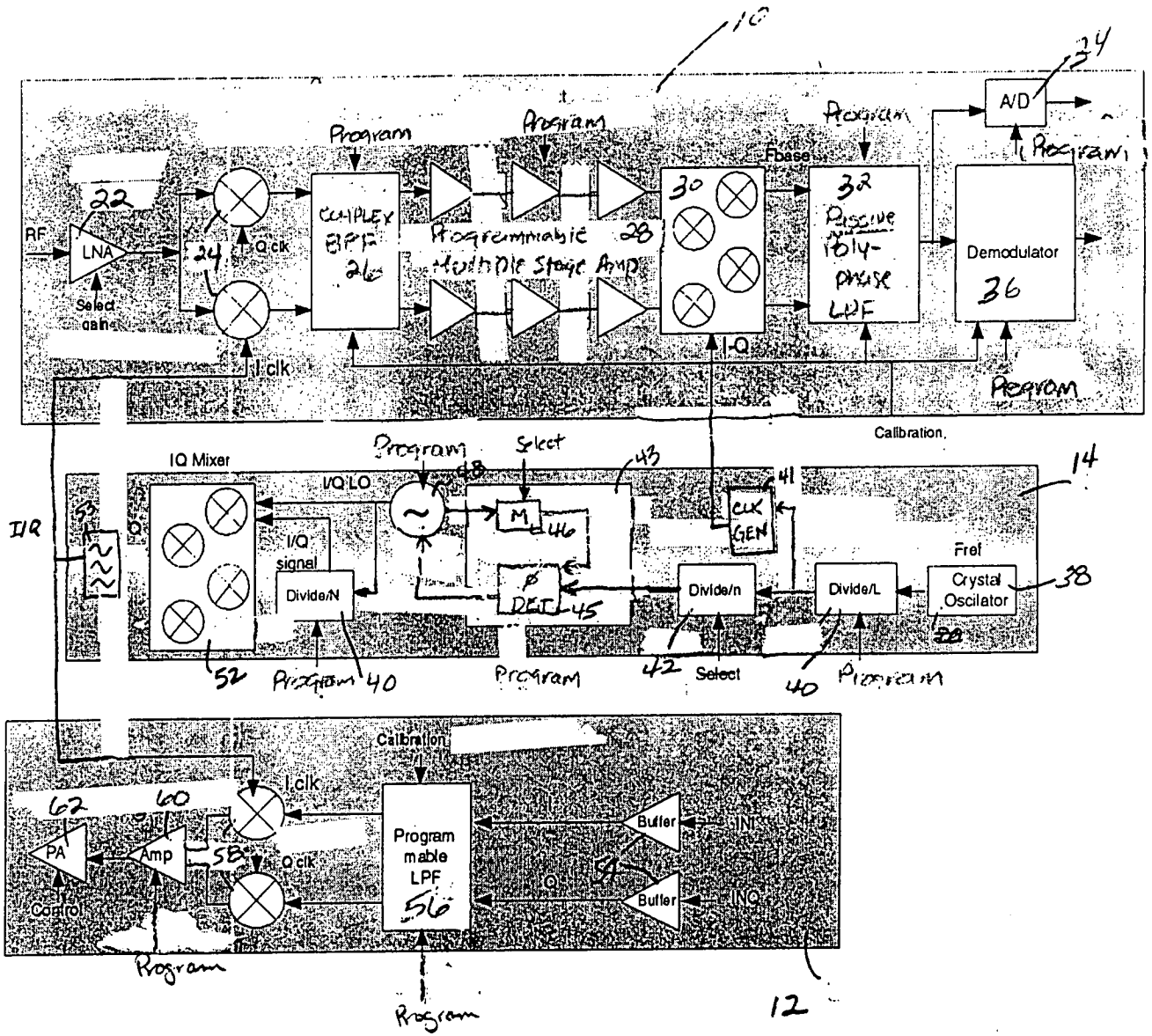


FIG. 2

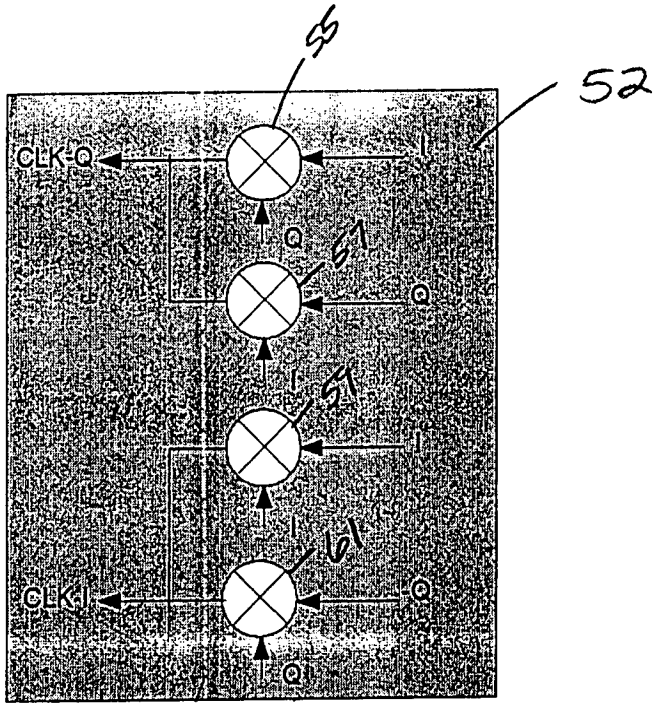


FIG. 3

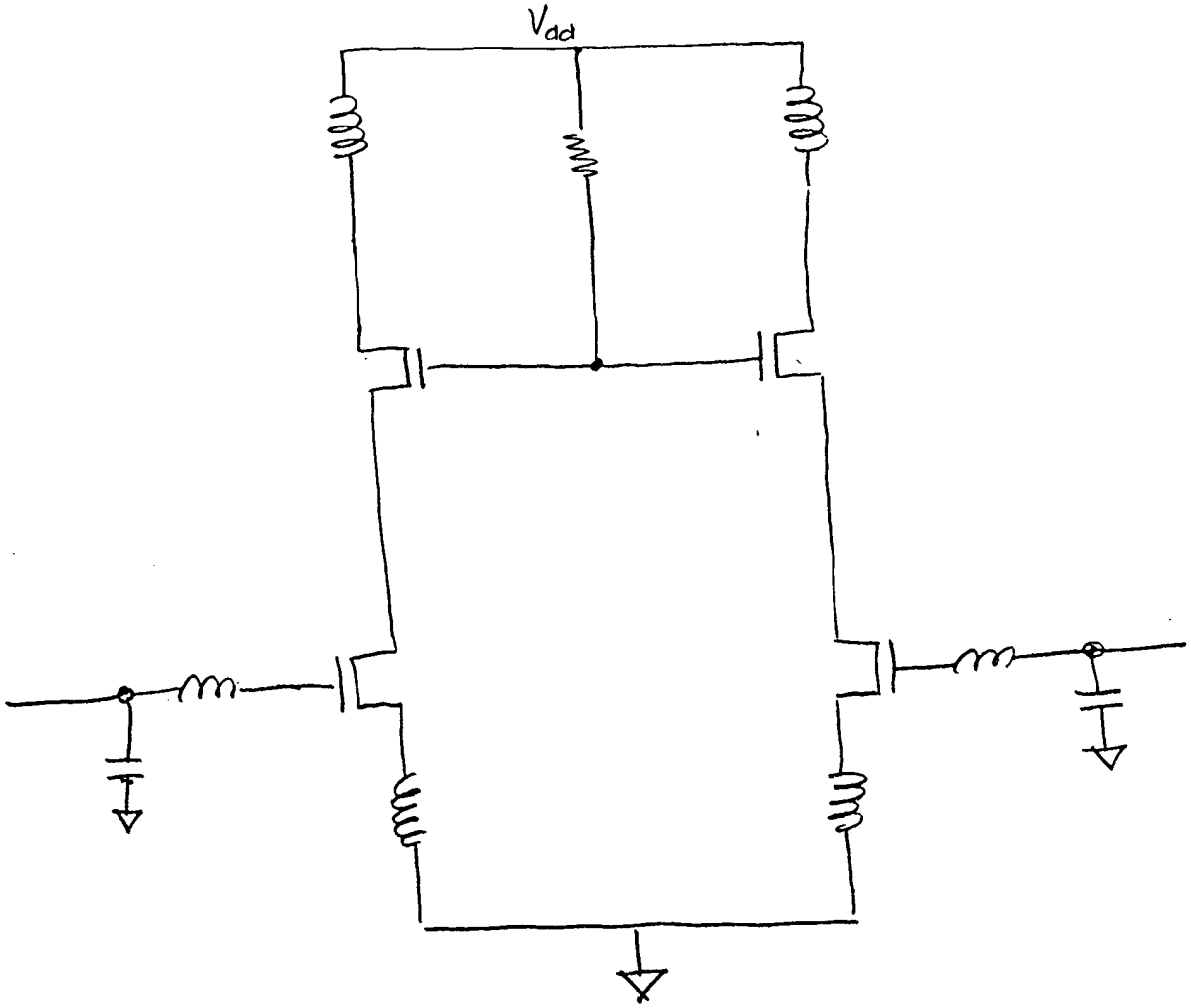


FIG. 4(a)

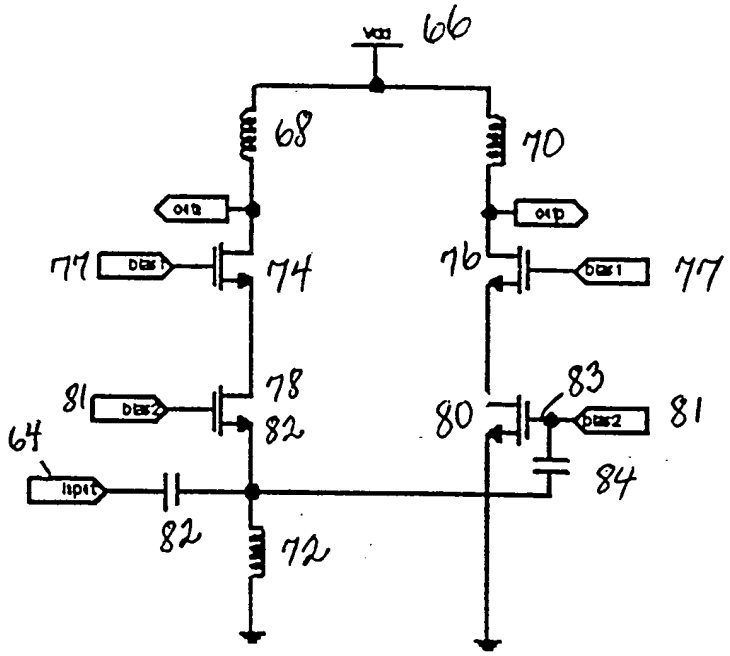

Vdd
66
68
70
outn
outp
77
bias1
74
76
bias1
77
81
bias2
78
83
80
bias2
81
64
82
inpt
84
82
72

FIG. 4

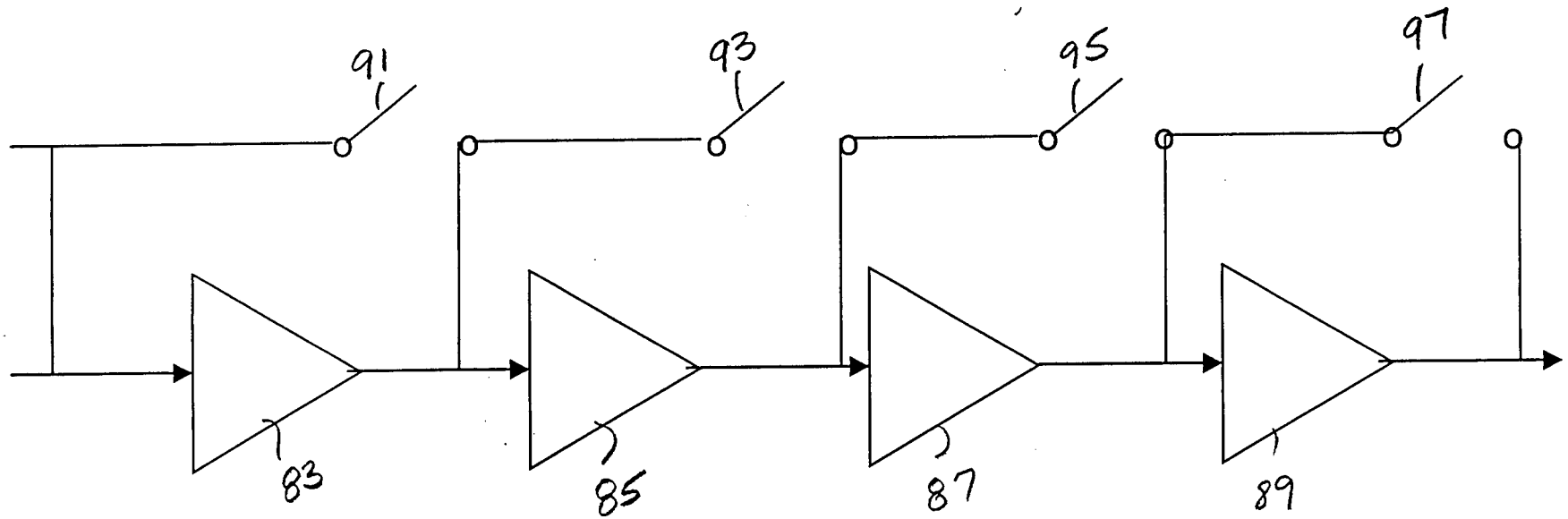


FIG. 5

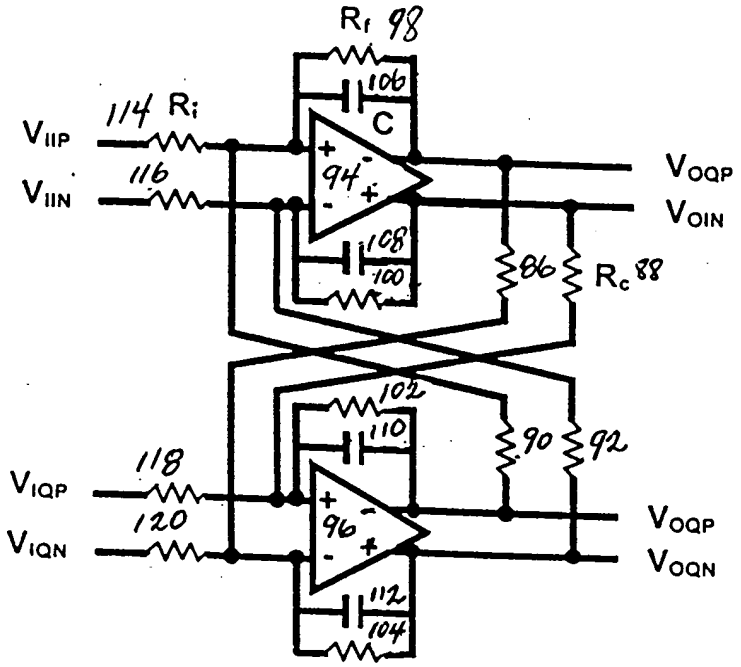

Rf 98
106
C
114 Ri
VIIP
116
VIIN
94
108
100
86
Rc 88
VOQP
VOIN
102
110
90
92
118
VIQP
120
VIQN
96
VOQP
VOQN
112
104

FIG. 6

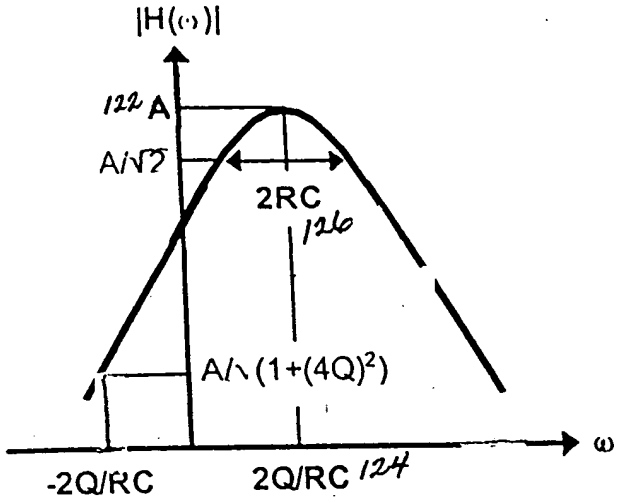


FIG. 7

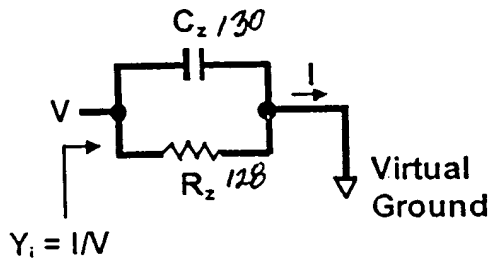


FIG. 8

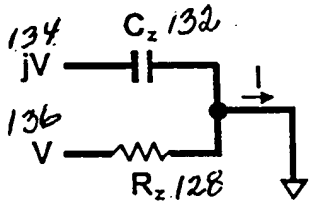


FIG. 9

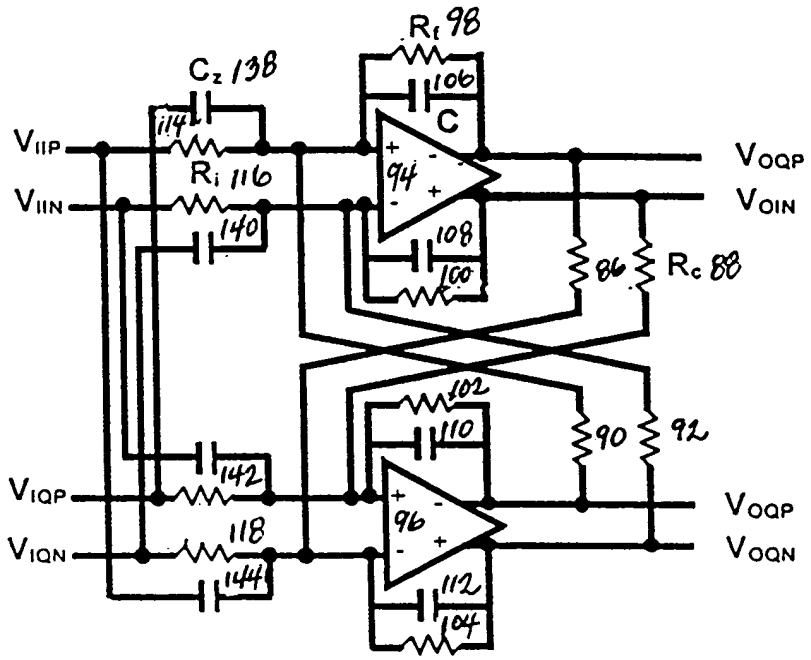

R_f 98
C_z 138
106
C
114
V_{IIP}
R_i 116
V_{IIN}
140
94
108
100
86
R_c 88
V_{OQP}
V_{OIN}
102
110
90
92
142
V_{IQP}
118
V_{IQN}
144
96
112
104
V_{OQP}
V_{OQN}

FIG. 10

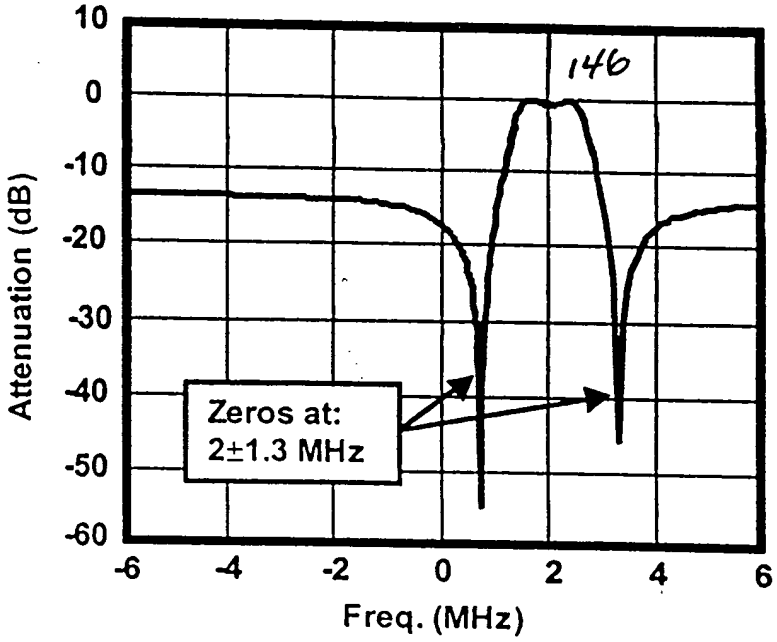

10
0
-10
-20
-30
-40
-50
-60
Attenuation (dB)
-6
-4
-2
0
2
4
6
Freq. (MHz)
146
Zeros at:
2±1.3 MHz

FIG. 11

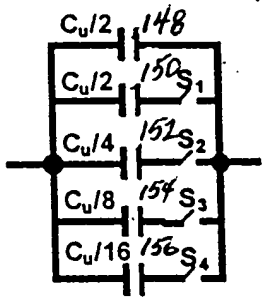


FIG. 12(a)

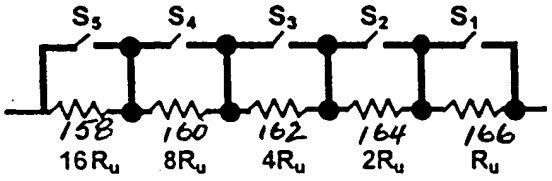


FIG. 12(b)

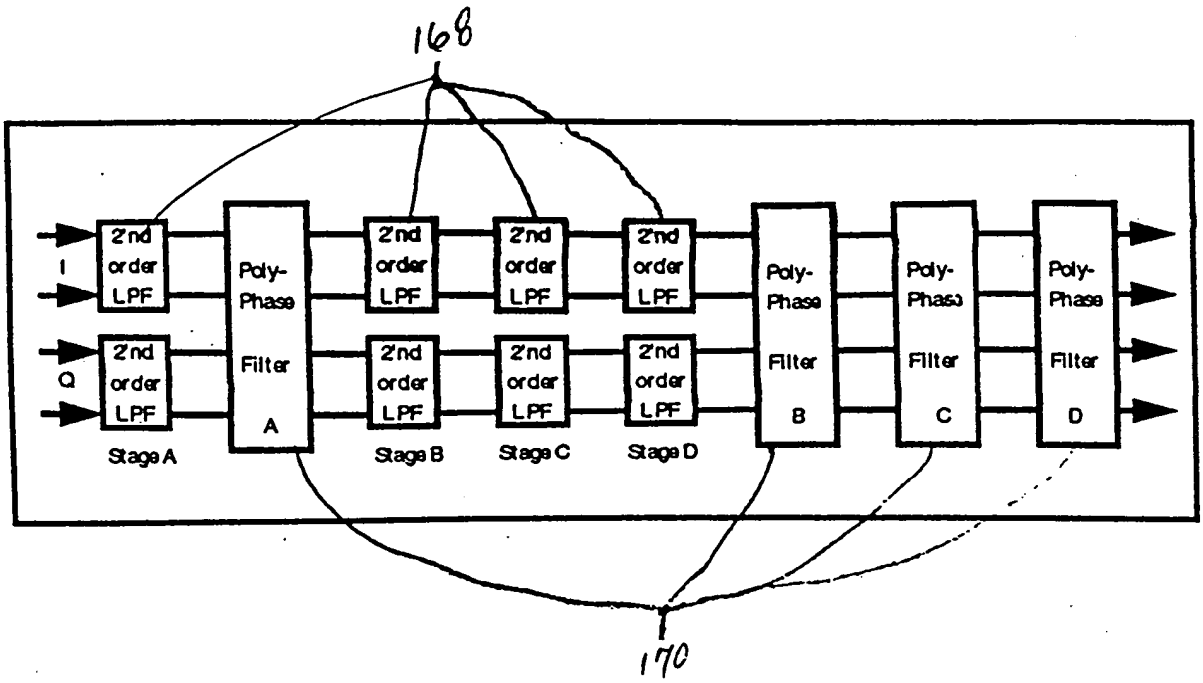

168
I
Q
2nd order LPF
Poly-Phase Filter A
Poly-Phase Filter B
Poly-Phase Filter C
Poly-Phase Filter D
Stage A
Stage B
Stage C
Stage D
170

FIG. 13

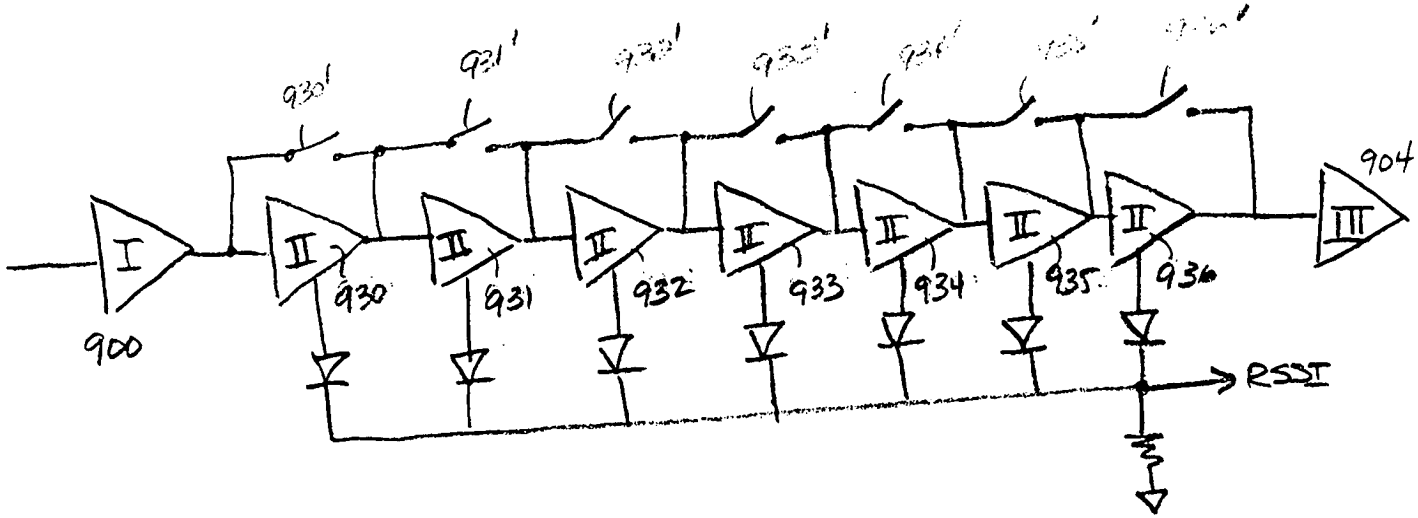


FIG. 14

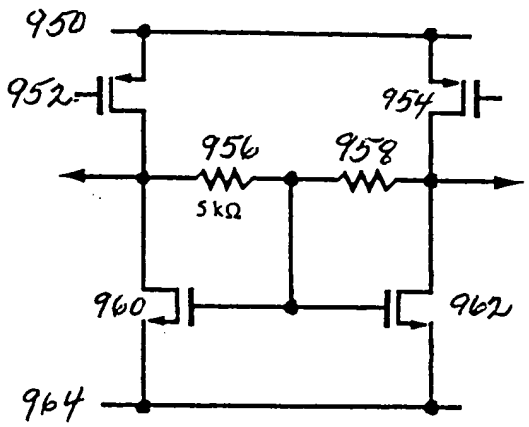


FIG. 15

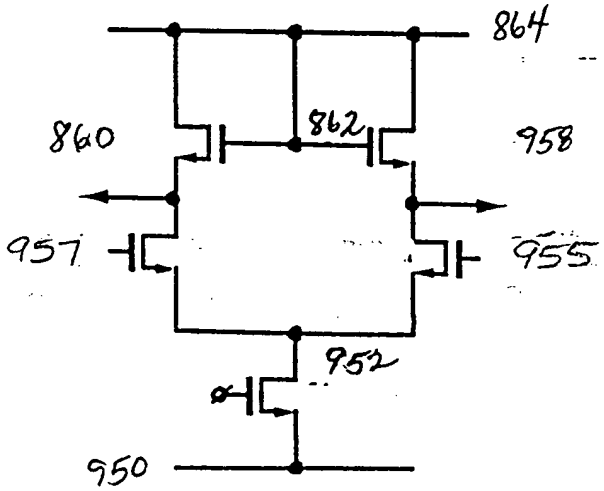


FIG. 16(a)

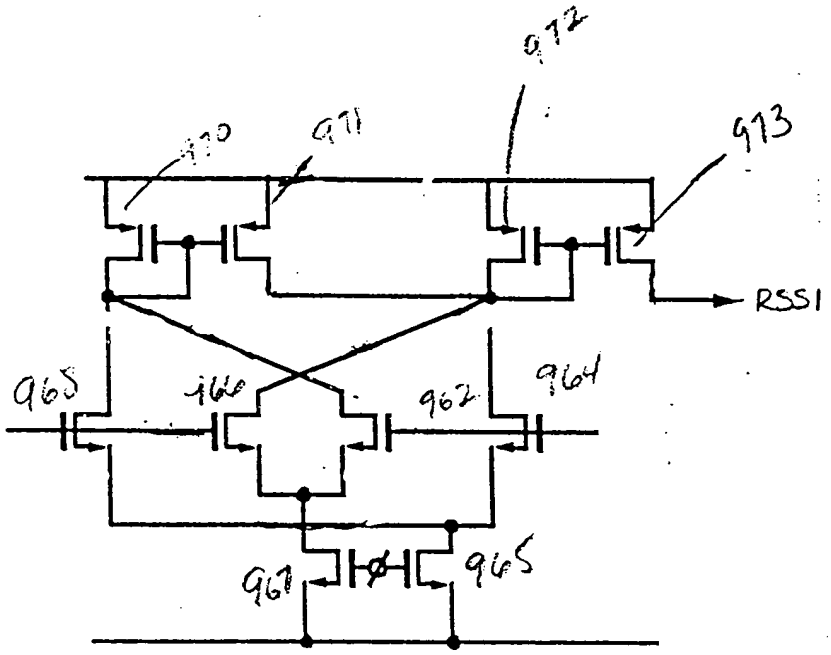


FIG. 16(b)

FIG. 17(a)

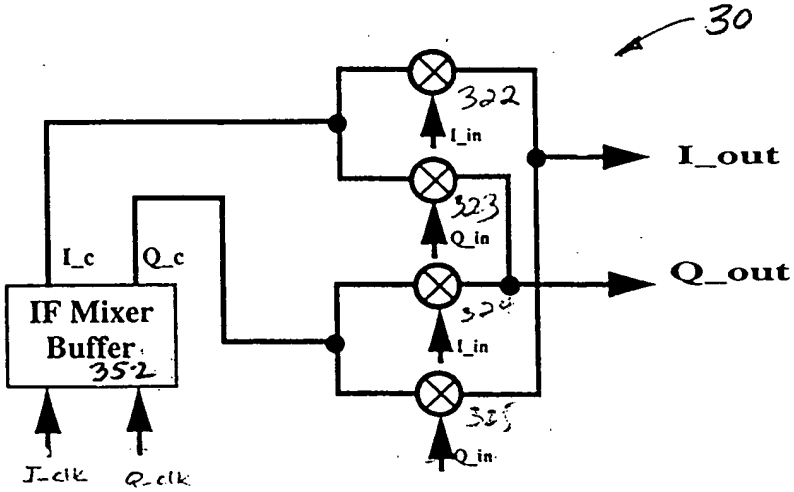
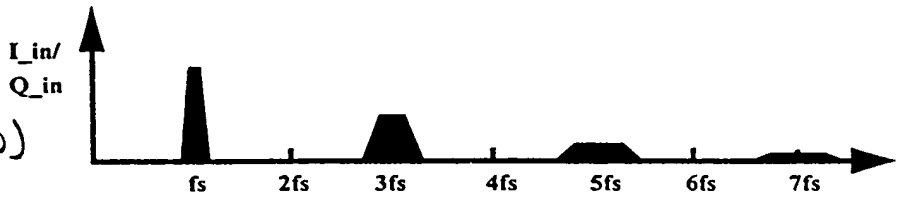
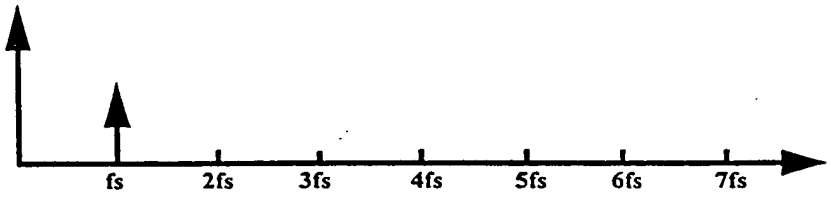


FIG. 17(b)



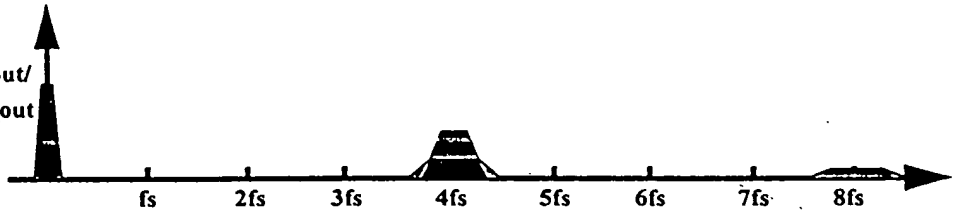
Limited IF Signal Spectrum

FIG. 17(c)



Sinusoidal Input Spectrum

FIG. 17(d)



IF MixerOutput Spectrum

FIG. 18

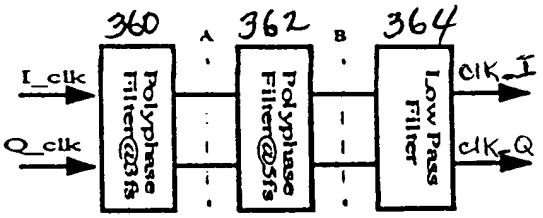
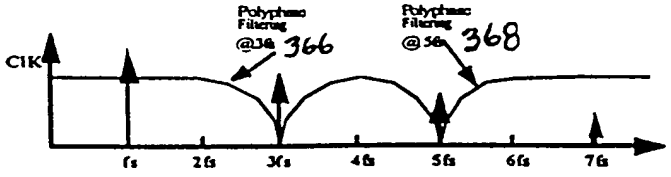
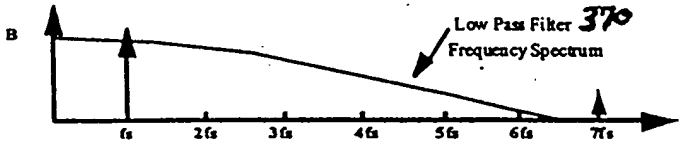


FIG. 19(a)



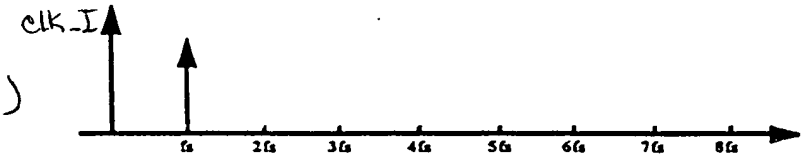
Differential Input Clock Signal Spectrum

FIG. 19(b)

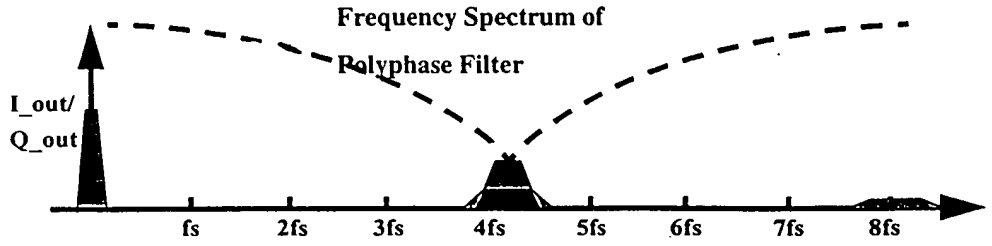


Signal Spectrum After 3fs and 5fs Polyphase

FIG. 19(c)

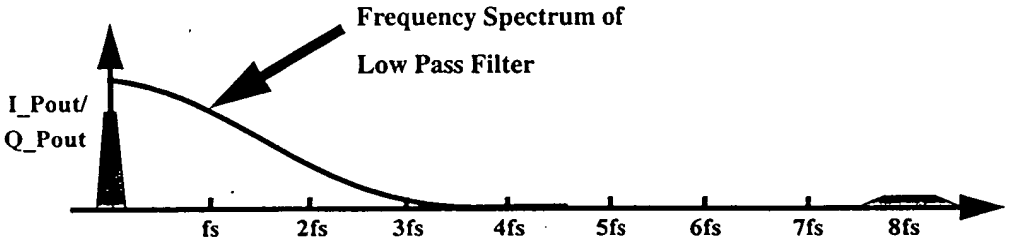


Sinusoidal Signal Generation After Low Pass Filter



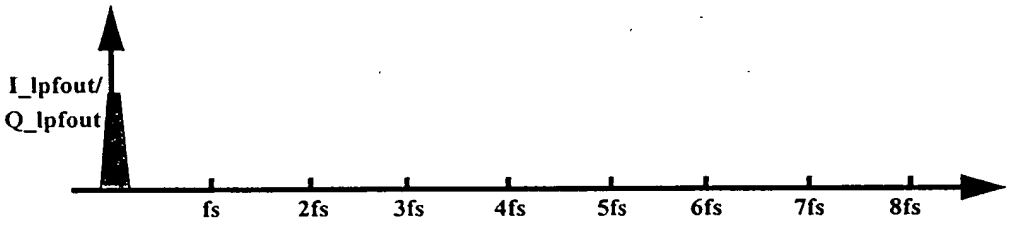
Signal Spectrum at Polyphase Input

FIG. 20(a)



Signal Spectrum at Polyphase Output

FIG. 20(b)



Signal Spectrum at Low Pass Filter Output

FIG. 20(c)

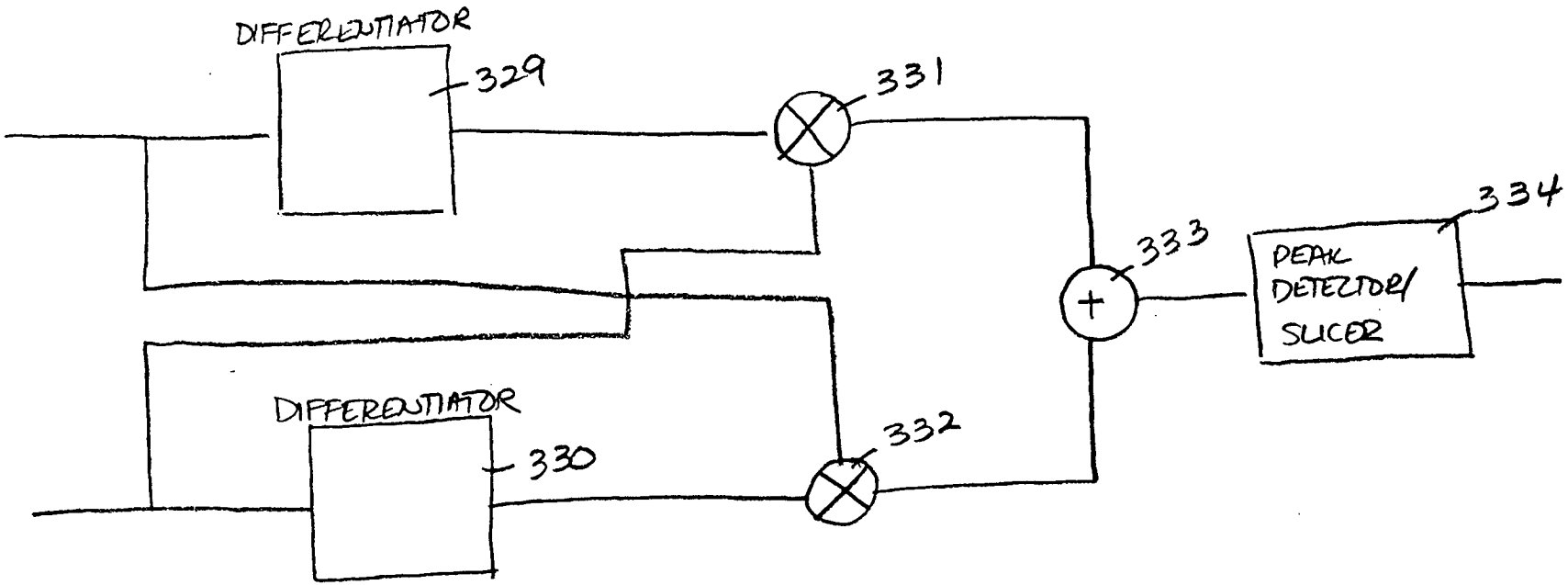

DIFFERENTIATOR
329
331
334
333
PEAK DETECTOR/ SLICER
DIFFERENTIATOR
330
332

FIG. 21

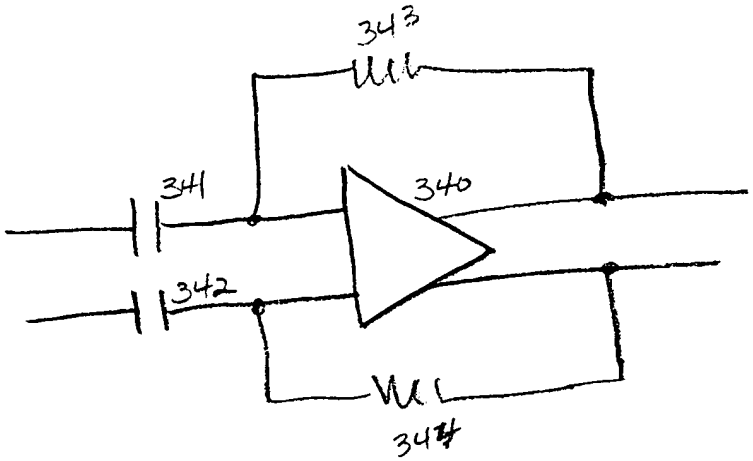


FIGURE 22

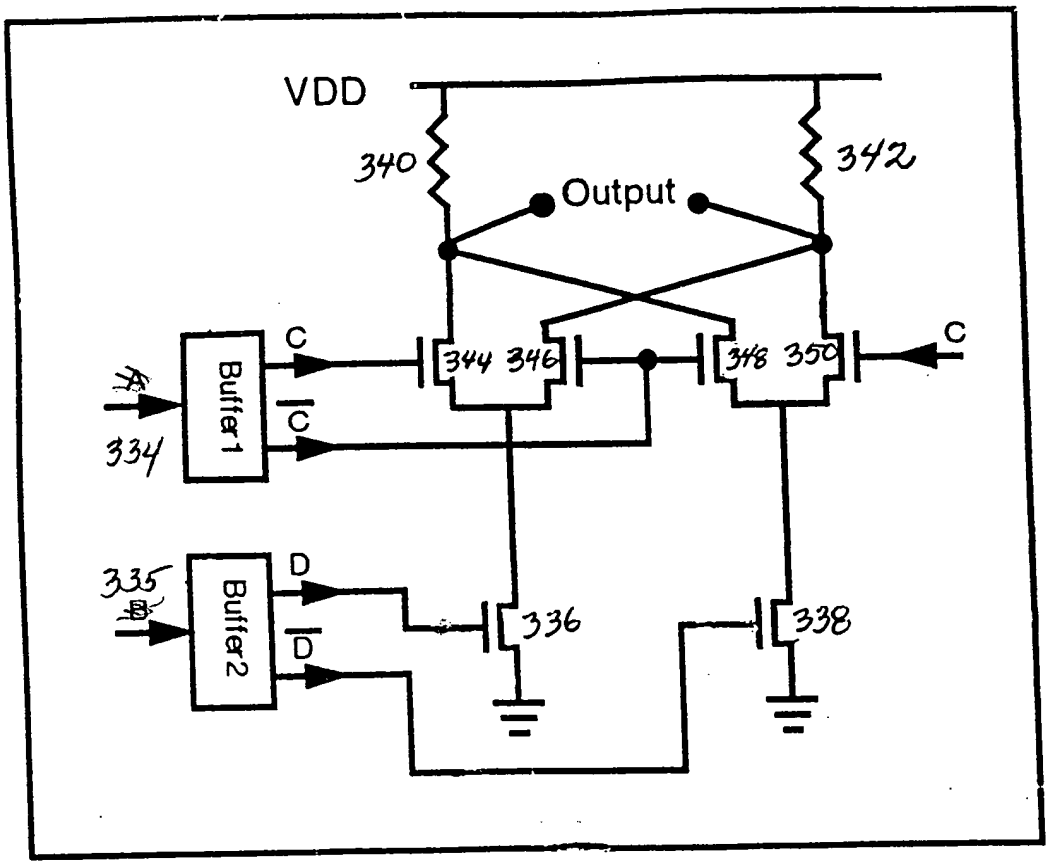

VDD
340
342
Output
C
344
346
348
350
C
Buffer1
A
334
C
335
B
Buffer2
D
D
336
338

FIG. 23

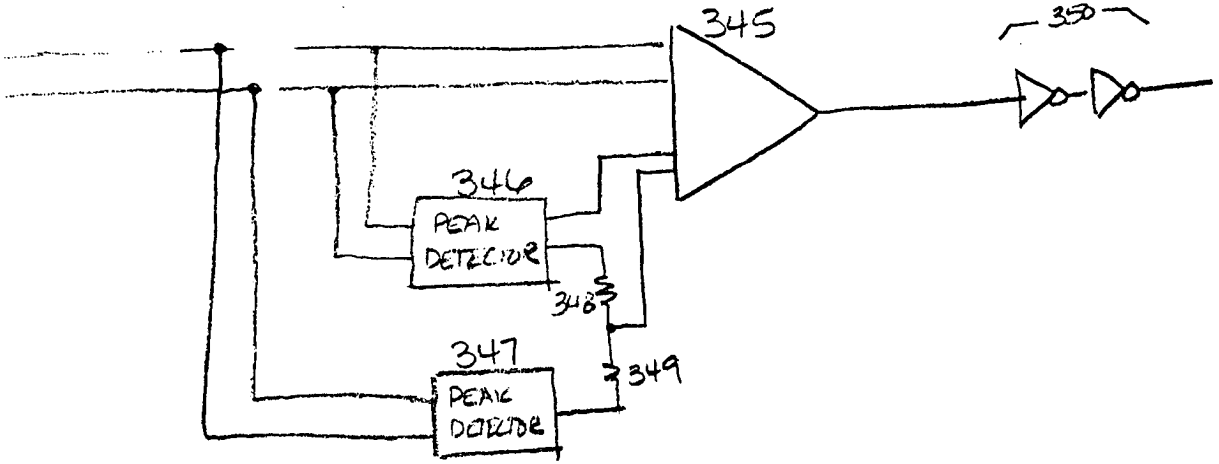


FIGURE 24

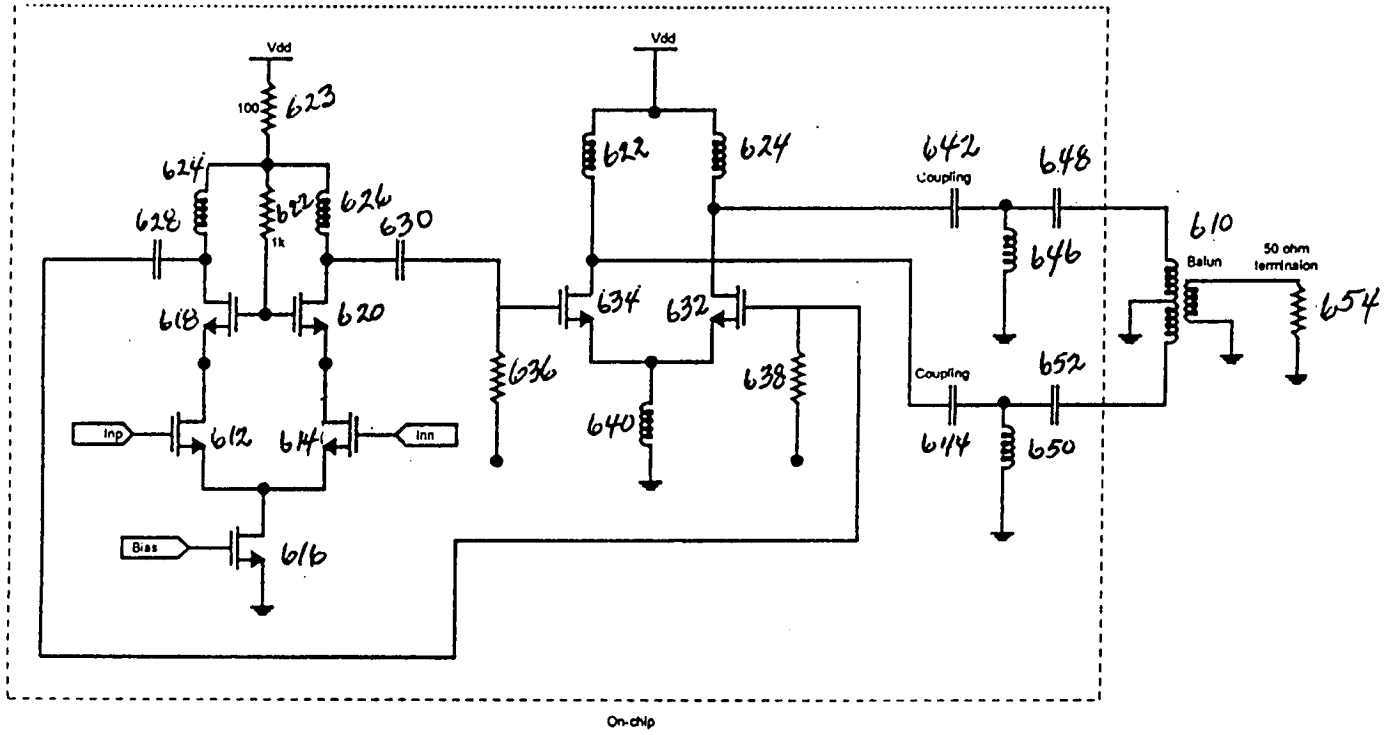


FIG. 25

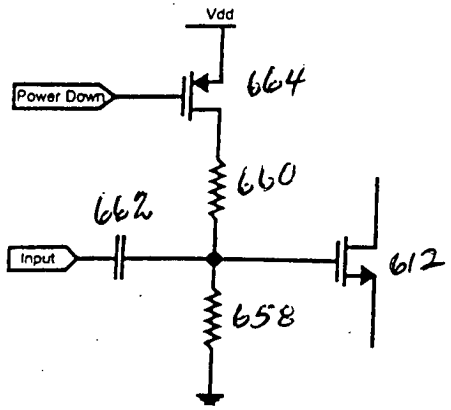


FIG. 26(a)

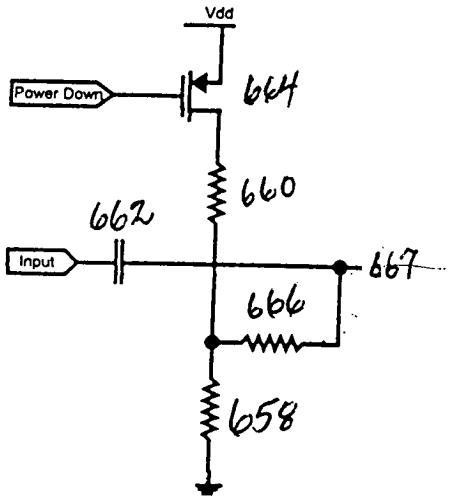


FIG. 26(b)

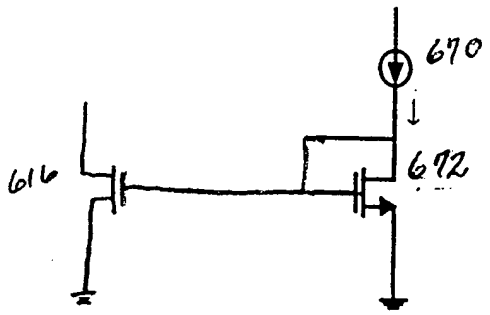


FIG. 27

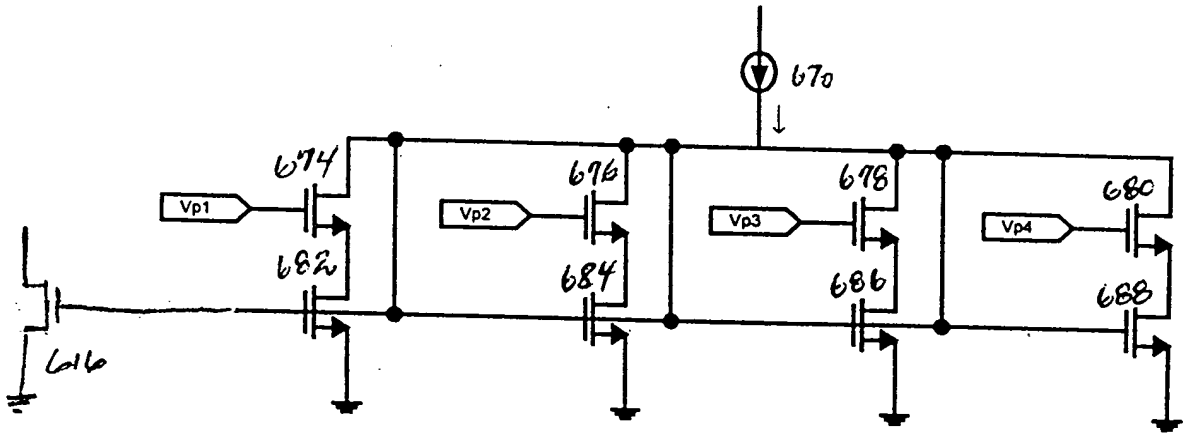


FIG. 28

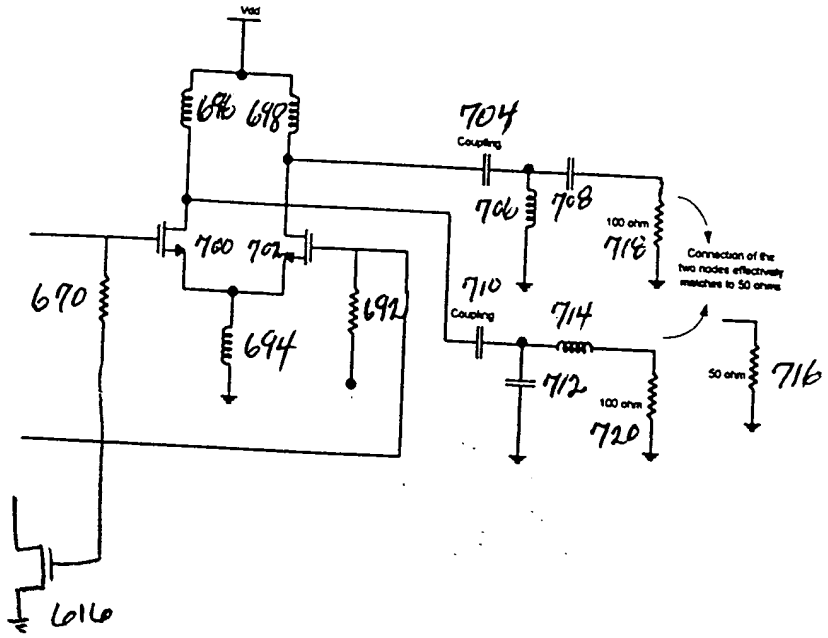

Vdd
696
698
704
Coupling
706
708
100 ohm
716
Connection of the two nodes effectively matches to 50 ohms
700
702
670
710
Coupling
714
694
692
712
100 ohm
720
50 ohm
716
6016

FIG. 29

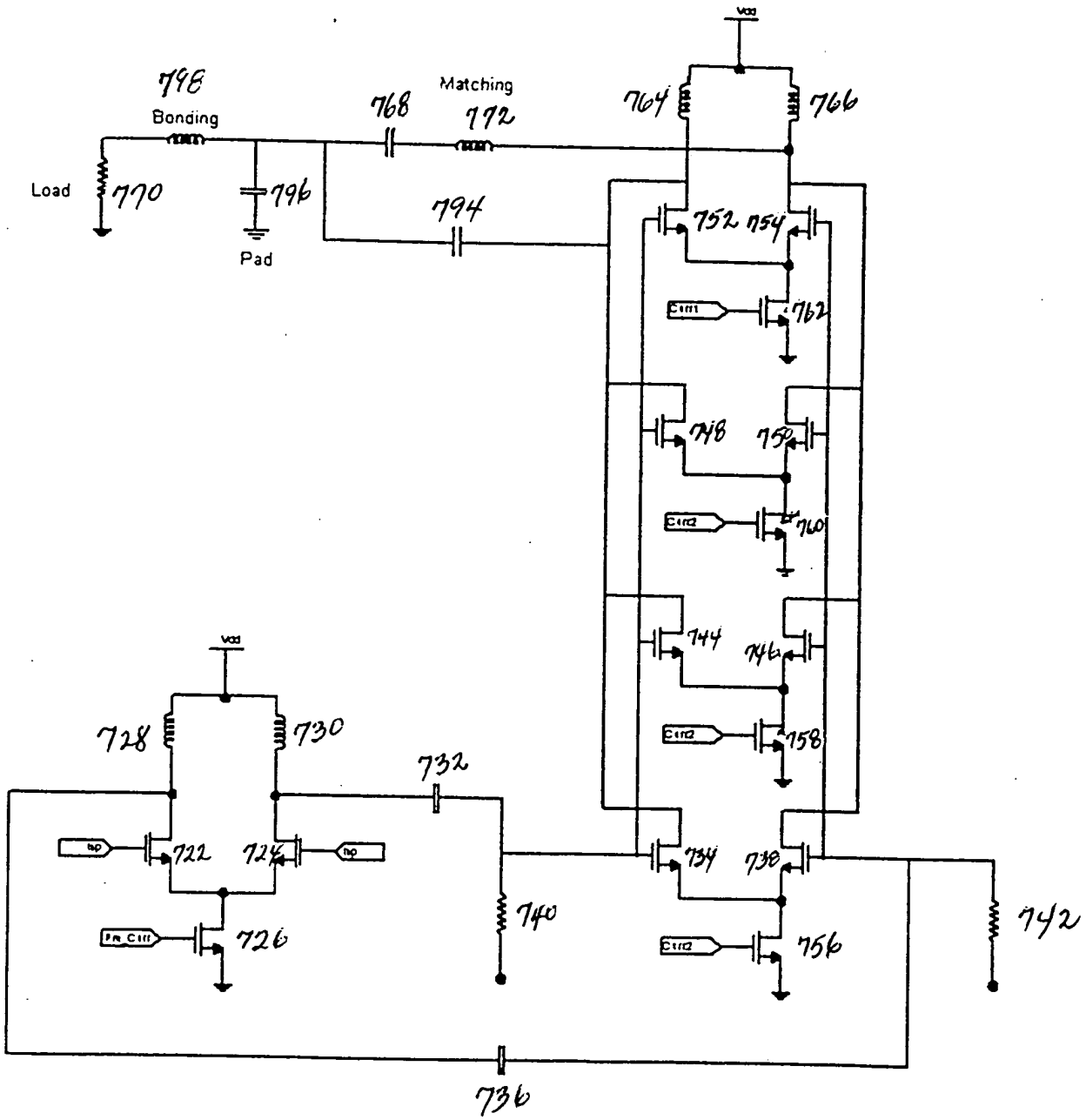

798
Bonding
768
Matching
772
764
766
Load
770
796
Pad
794
752
754
762
748
750
760
744
746
758
728
730
732
722
724
726
734
736
740
742
756

FIG. 30

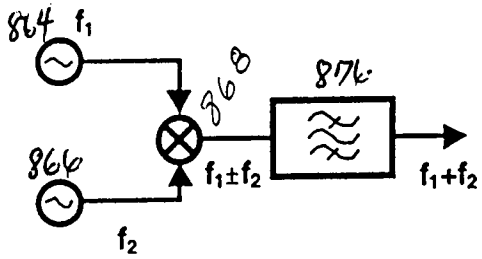


FIG. 31 (a)

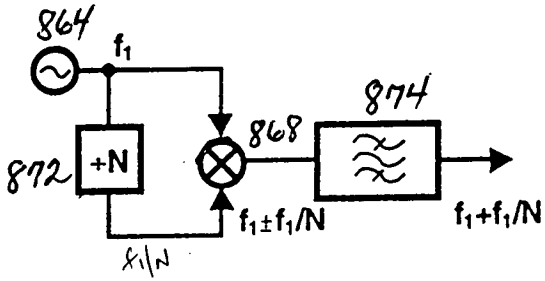


FIG. 31 (b)

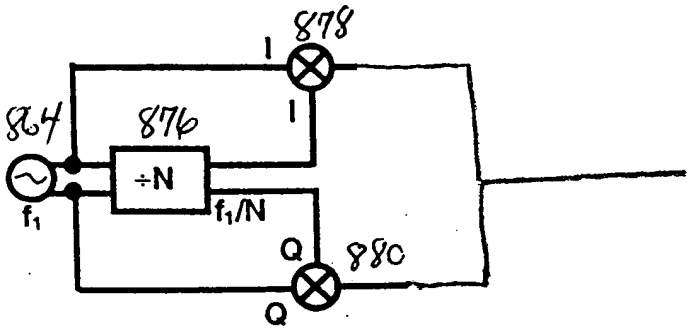


FIG. 32

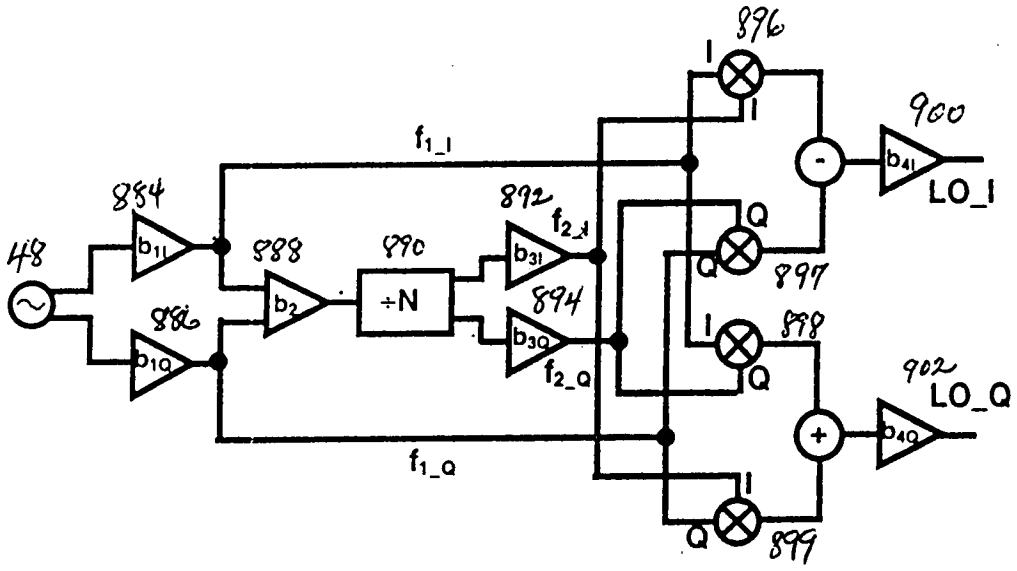

48
884
886
888
890
892
894
896
897
898
899
900
902
b_{1I}
b_{1Q}
b_2
÷N
b_{3I}
b_{3Q}
f_{2_I}
f_{2_Q}
f_{1_I}
f_{1_Q}
b_{4I}
b_{4Q}
LO_I
LO_Q
I
Q
-
+

FIG. 33

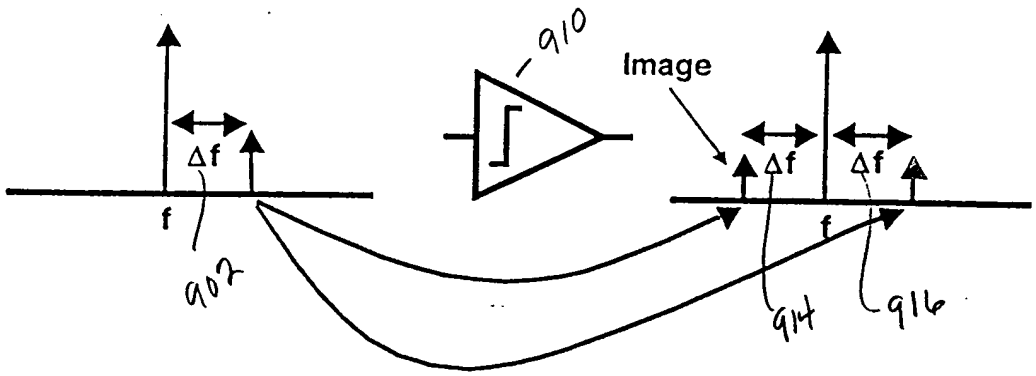


FIG. 33(a)

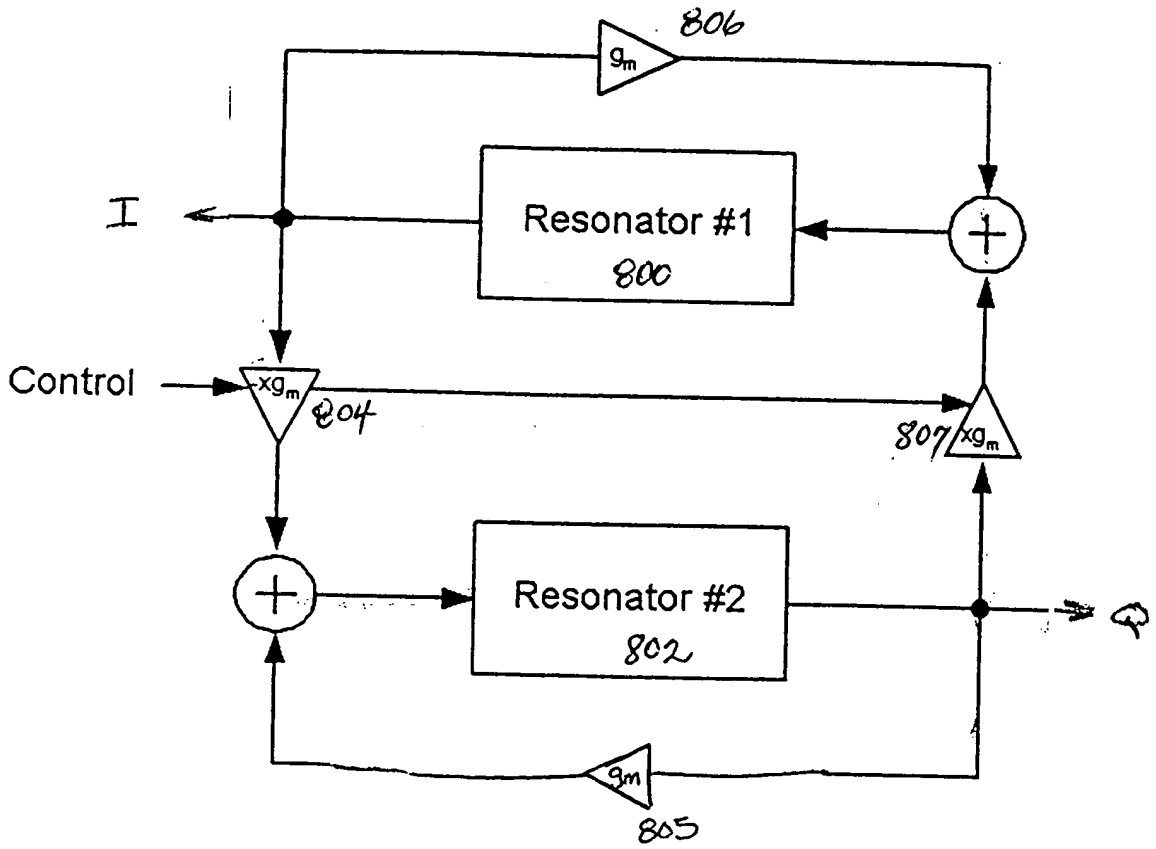

806
g_m
I
Resonator #1
800
Control
xg_m
804
807
xg_m
Resonator #2
802
Q
g_m
805

FIG. 34

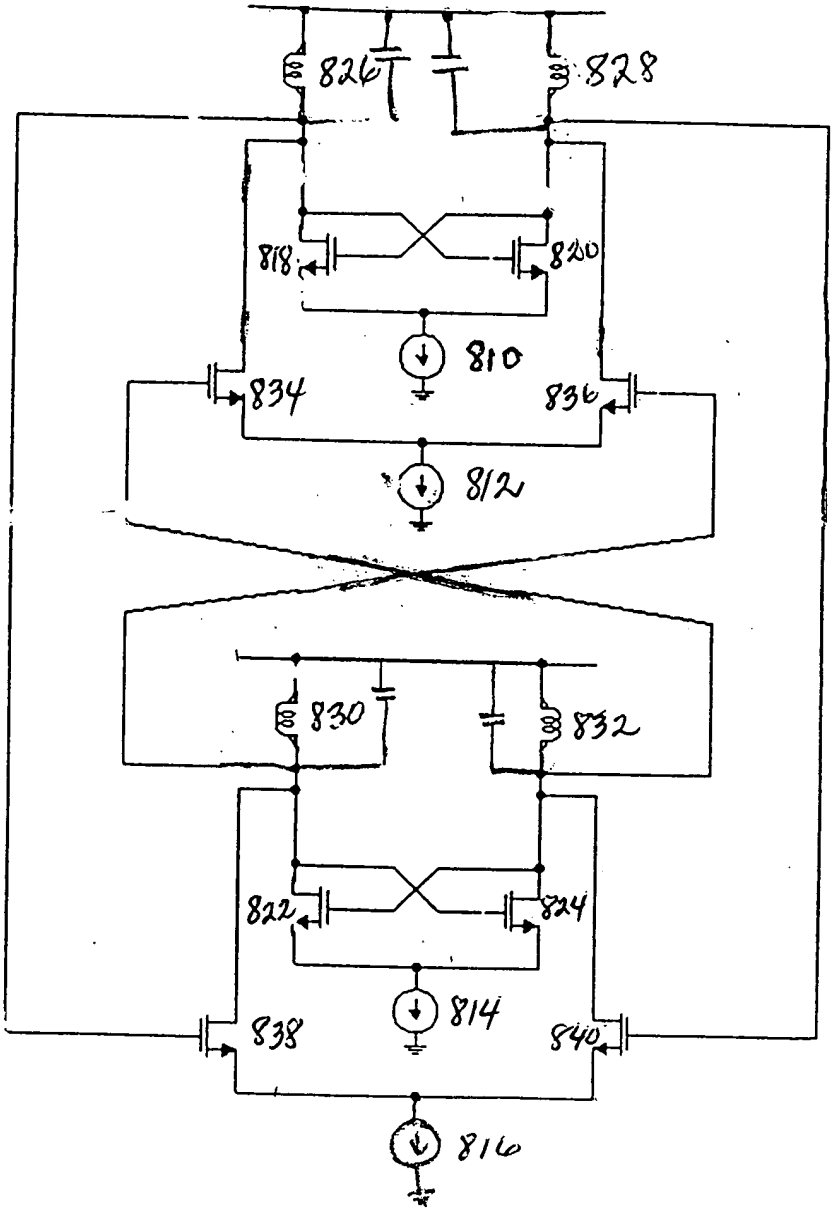
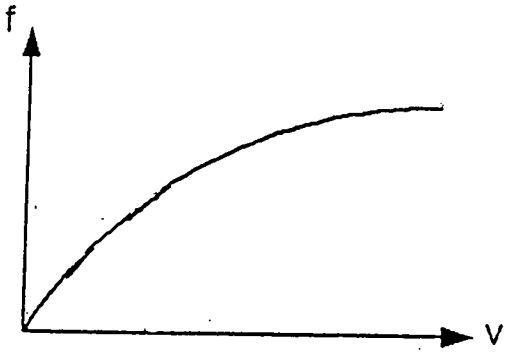
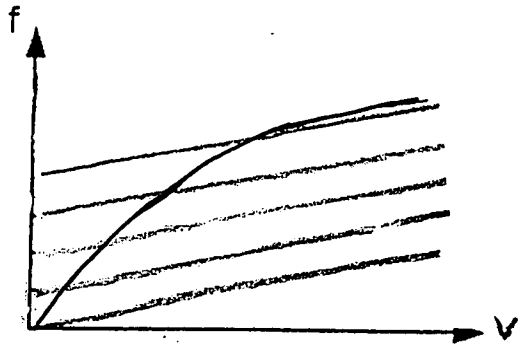


FIG. 35



Typical VCO Tuning Curve

FIG. 36(a)



Segmented VCO Tuning Curve

FIG. 36(b)

FIG. 37(a)

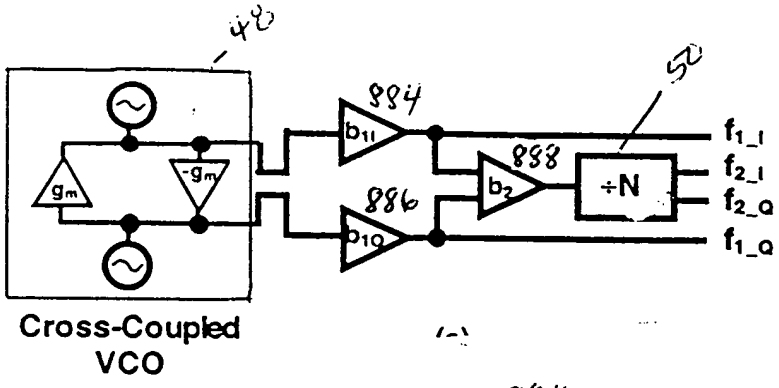
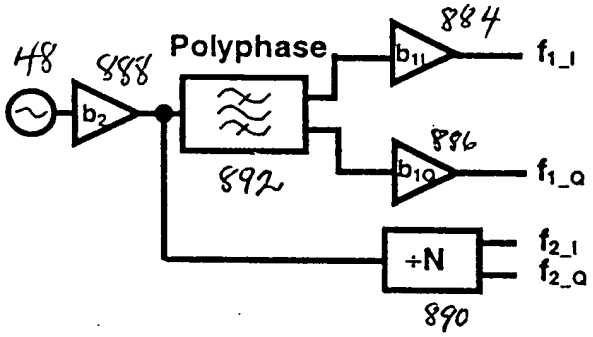


FIG. 37(b)



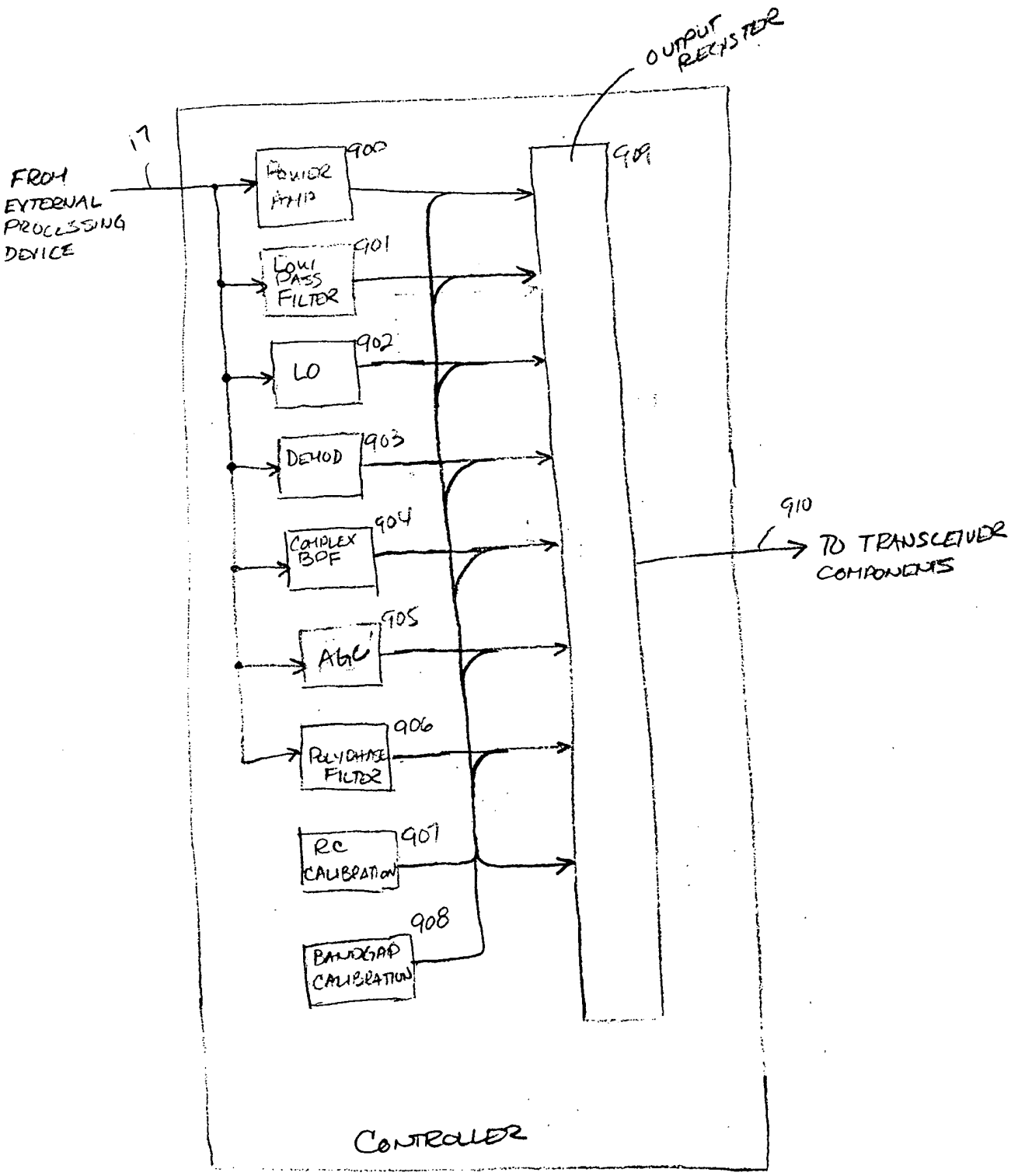

FROM EXTERNAL PROCESSING DEVICE
17
POWER AMP
900
LOW PASS FILTER
901
LO
902
DEMOD
903
COMPLEX BPF
904
AGC
905
POLYPHASE FILTER
906
RC CALIBRATION
907
BANDGAP CALIBRATION
908
OUTPUT REGISTER
909
910
TO TRANSCEIVER COMPONENTS
CONTROLLER

FIGURE 38

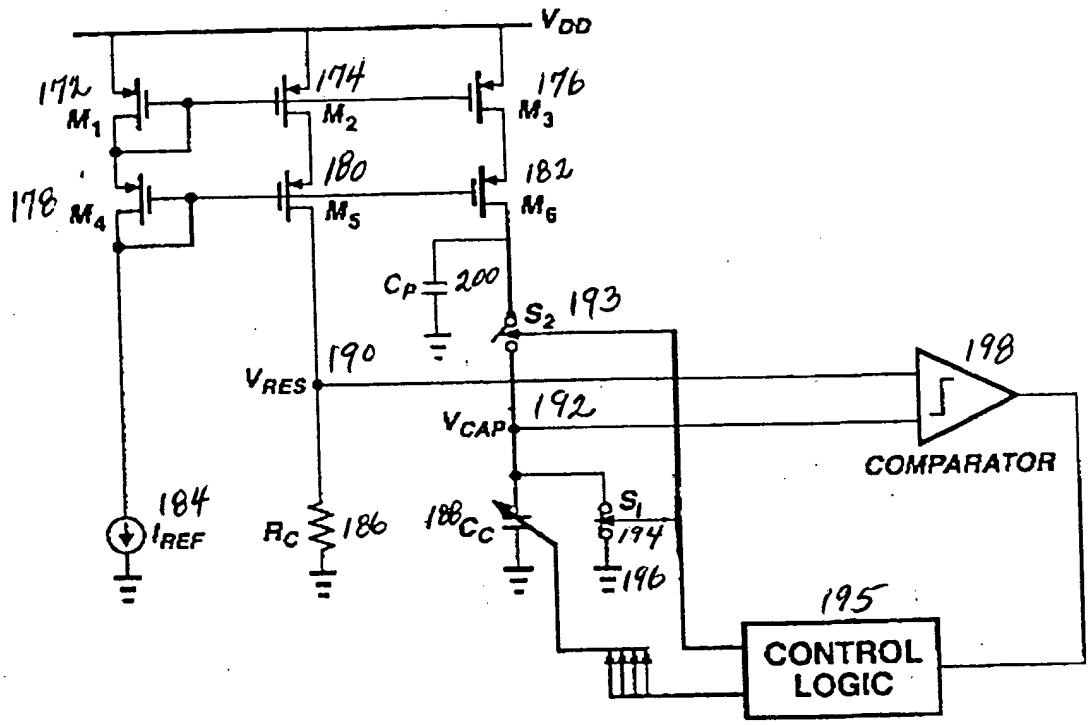

V_{DD}
172
M_1
174
M_2
176
M_3
178
M_4
180
M_5
182
M_6
C_P 200
S_2 193
190
V_{RES}
192
V_{CAP}
198
COMPARATOR
184
I_{REF}
R_C 186
188 C_C
S_1 194
196
195
CONTROL LOGIC

FIG. 39

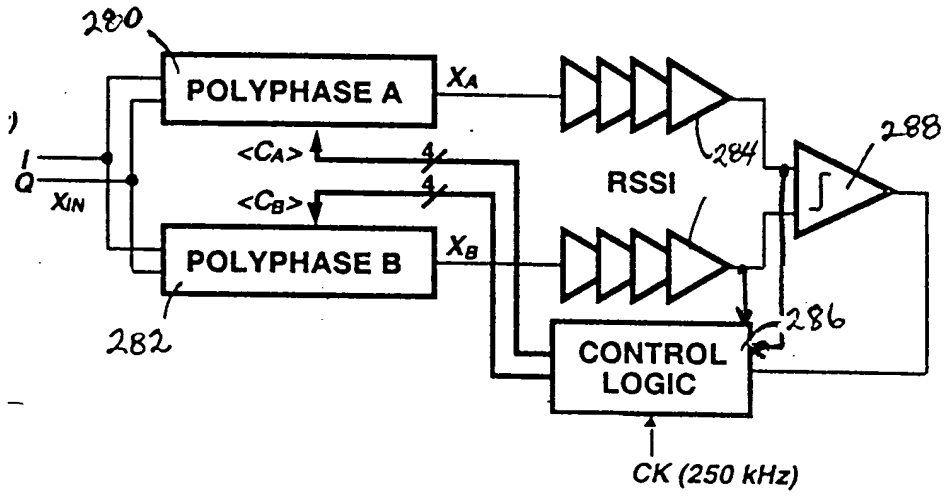

280
POLYPHASE A
X_A
$<C_A>$
4
I
Q
X_{IN}
$<C_B>$
4
RSSI
284
288
POLYPHASE B
X_B
282
286
CONTROL LOGIC
CK (250 kHz)

FIG. 40

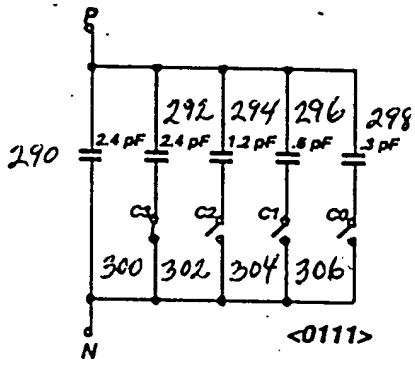

P
290
2.4 pF
292
2.4 pF
294
1.2 pF
296
.6 pF
298
.3 pF
C3
C2
C1
C0
300
302
304
306
N
<0111>

FIG. 41

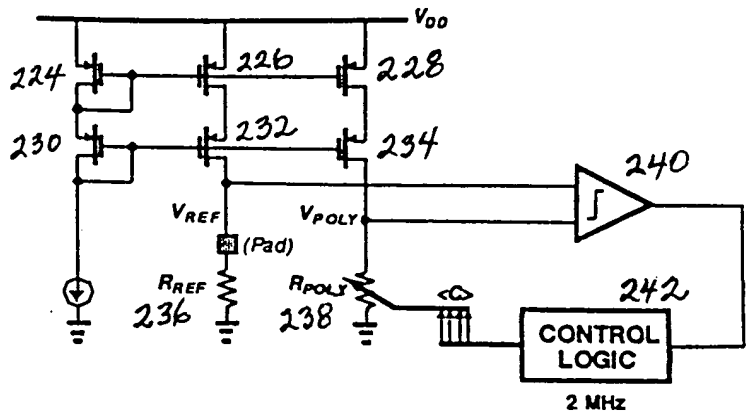

V_{DD}
224
226
228
230
232
234
240
V_{REF}
V_{POLY}
(Pad)
R_{REF}
236
R_{POLY}
238
242
CONTROL LOGIC
2 MHz

FIG. 42

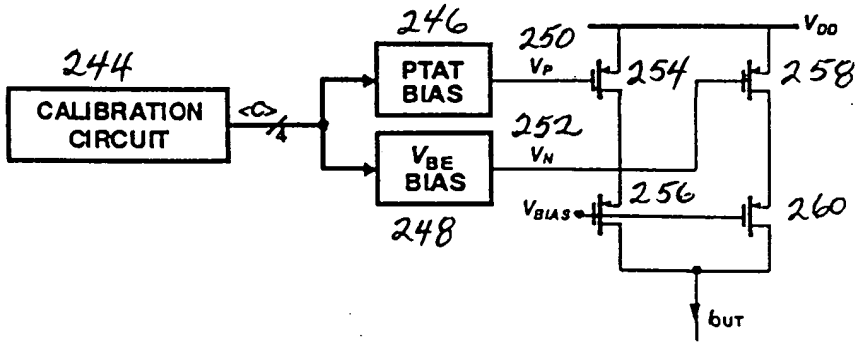

244
CALIBRATION CIRCUIT
<C>
4
246
PTAT BIAS
V_{BE} BIAS
248
250
V_P
252
V_N
V_{BIAS}
254
256
258
260
V_{DD}
I_{OUT}

FIG. 43

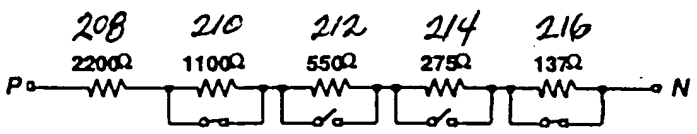


FIG. 44

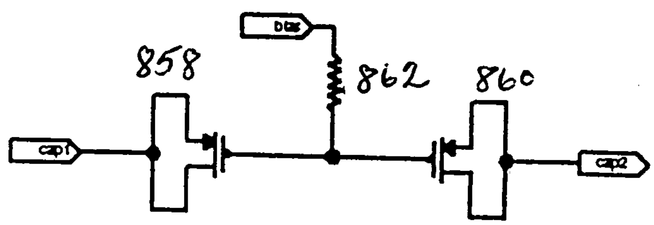


FIG. 45

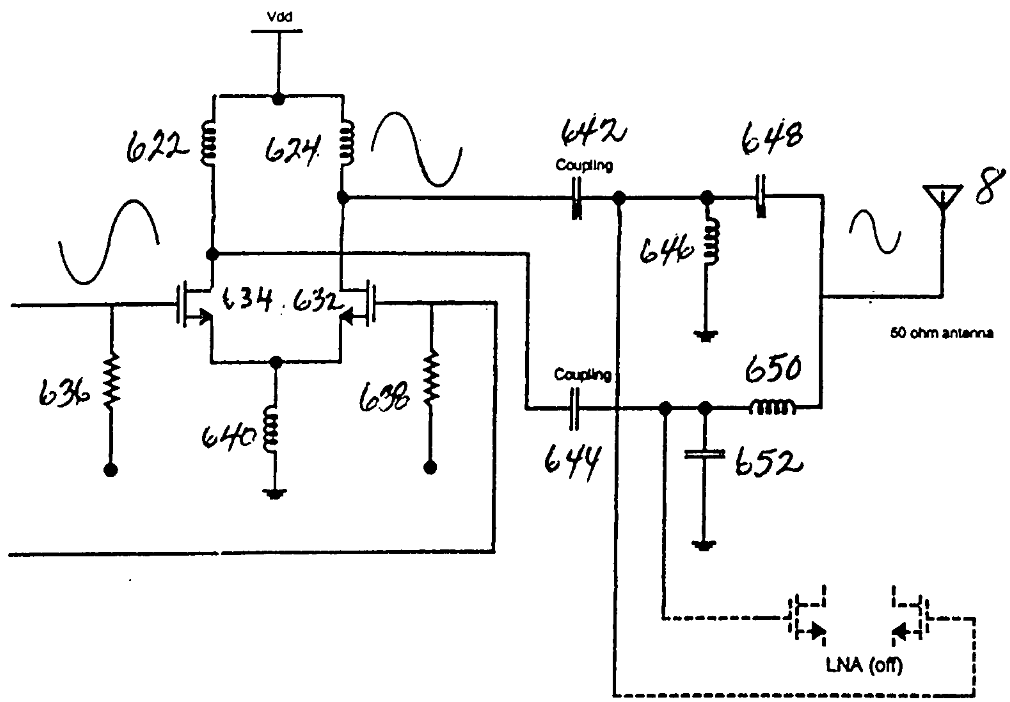

Vdd
622
624
642
Coupling
648
8
646
634
632
50 ohm antenna
636
638
640
Coupling
650
644
652
LNA (off)

FIG. 46

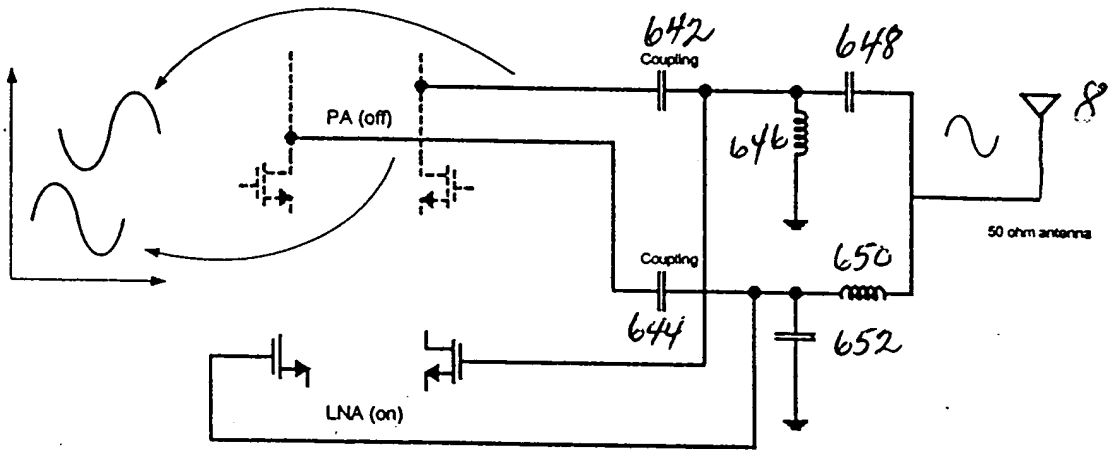

642
Coupling
648
PA (off)
646
8
50 ohm antenna
Coupling
650
644
652
LNA (on)

FIG. 47